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2666

**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

4

Application Number

10/075,086

Filing Date

02/10/02

First Named Inventor

Haw-minn Lu

Art Unit

2666

Examiner Name

Seema Srinivas Rao

Attorney Docket Number

ENCLOSURES (Check all that apply)☐

Fee Transmittal Form

☐

Fee Attached

☐

Amendment/Reply

☐

After Final

☐

Affidavits/declaration(s)

☐

Extension of Time Request

☐

Express Abandonment Request

☒

Information Disclosure Statement

☐

Certified Copy of Priority Document(s)

☐Response to Missing Parts/
Incomplete Application☐Response to Missing Parts
under 37 CFR 1.52 or 1.53☐

Drawing(s)

☐

Licensing-related Papers

☐

Petition

☐Petition to Convert to a
Provisional Application☐Power of Attorney, Revocation
Change of Correspondence Address☐

Terminal Disclaimer

☐

Request for Refund

☐

CD, Number of CD(s) _____

☐After Allowance communication
to Group☐Appeal Communication to Board
of Appeals and Interferences
Appeal Communication to Group
(Appeal Notice, Brief, Reply Brief)☐

Proprietary Information

☐

Status Letter

☒Other Enclosure(s) (please
Identify below):USPTO Form SB/08B (1pg); Cited References
(1 reference total); Return Postcard

Remarks

Information Disclosure Statement is submitted herewith as 2
pages.**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**Firm
or
Individual name

Haw-minn Lu

Signature

Date

December 4, 2004

CERTIFICATE OF TRANSMISSION/MAILING

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Typed or printed name

Haw-minn Lu

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Date

December 4, 2004

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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In the United States Patent and Trademark Office

Serial Number: 10/075,086
Application. Filed: 02/10/02
Applicant(s): Haw-minn Lu
Application. Title: Fanout Upgrade for a Scalable Switching Network
Examiner: Seema Srinivas Rao
Group Art Unit: 2666

Mailed: 2004 December 4
At: San Diego, CA

Information Disclosure Statement

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

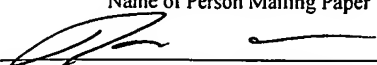
In compliance with the Applicant's duty under 37 CFR 1.97-98, the following information is brought to the attention of the Examiner. The items are listed on the attached Form PTO/SB/08B and copies of each are enclosed for the convenience of the Examiner. The Applicant respectfully requests that the documents be made of record in the above-referenced application.

CERTIFICATE OF MAILING
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with anything referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

December 4, 2004
Date of Deposit

Haw-minn Lu
Name of Person Mailing Paper


Signature of Person Mailing Paper

The items identified in this Information Disclosure Statement were cited by Applicant in response filed on October 4, 2004 to an office action mailed on May 3, 2004 by examiner Derrick W. Ferris directed towards U.S. Patent Application No. 09/897,263, filed on July 2, 2001 by the Applicant.

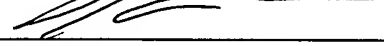
The items identified in this Information Disclosure Statement may or may not be "material" pursuant to 37 CFR 1.56. The submission of this document thereof by the Applicant shall not be construed as an admission that any such patent, publication or other information referred to therein is material or considered to be material (37 CFR 1.97(h)), or even qualifies as "prior art" under 35 U.S.C. § 102 with respect to this invention unless specifically designated by the Applicant as such.

The filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information, as defined in 37 CFR 1.56, exists.

The attached IDS is being filed in accordance with 37 CFR §§ 1.97 and 1.98. This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b)(3), that is before the mailing of a first Office Action on the merits. Thus, no petition or fee is required.

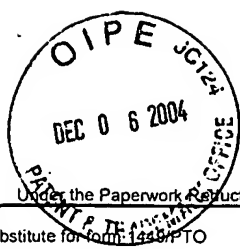
Respectfully submitted,

Dated: December 4, 2004

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	10/075,086
		Filing Date	02/10/02
		First Named Inventor	Haw-minn Lu
		Art Unit	2666
		Examiner Name	Seema Srinivas Rao
Sheet 1	of 1	Attorney Docket Number	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		AGRAWAL, "Testing and Fault-Tolerance of Multistage Interconnection Networks," Computer, Apr. 1982, pp. 41-53, vol. 15, no. 4, IEEE, US	
		BHUYAN, et. al "Design and Performance of Generalized Interconnection Networks." IEEE Transactions on Computers, Dec. 1983, pp. 1081-1090, vol. 32, no. 12, IEEE, US	
		BLAKE, et. al "Multistage Interconnection Network Reliability," IEEE Transactions on Computers, Nov. 1989, pp. 1600-1603, vol. 38, no. 11, IEEE, US	
		CHIN, et. al "Packet Switching Networks for Multiprocessors and Data Flow Computers," IEEE Transactions on Computers, Nov. 1984, pp. 991-1003, vol. 33, no. 11, IEEE, US	
		KUMAR, et. al "Failure Dependent Performance Analysis of a Fault-Tolerant Multistage Interconnection Network," IEEE Transactions on Computers, Dec. 1989, pp. 1703-1713, vol. 38, no. 12, IEEE, US	
		TZENG, et. al "Realizing Fault-Tolerant Interconnection Network via Chaining," IEEE Transactions on Computers, Apr. 1988, pp. 458-462, vol. 37. no. 4, IEEE, US	
		VARMA, et. al "Fault-Tolerant Routing in Multistage Interconnection Networks," IEEE Transactions on Computers, Mar. 1989, pp. 385-393, vol. 38, no. 3, IEEE, US	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.
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